Notice of References Cited Application/Control No. 10/605,232 Examiner Stanetta D. Isaac Applicant(s)/Patent Under Reexamination LIN, HUI-CHU Art Unit Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,719,065	02-1998	Takemura et al.	438/163
	В	US-5,940,732	08-1999	Zhang, Hongyong	438/640
	С	US-		•	
	D	US-			
	E	US-			
	F	US-			•
	G	US-			
	Н	US-			
		US-			
	J	US-			
	κ	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
:	N-					
	0					
	Р					
	Q					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, Second Edition, pgs. 170-180 and pgs. 202-206				
	V					
	w					
	X					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.